

Delphion

RESEARCH

INTEGRATED IAM

SERVICES

INSIDE DELPHION

Log Out

Work Files

Saved Searches

My Account | Products | News | Events

Search: Quick/Number Boolean Advanced

The Delphion Integrated View: INPADOC Record

Buy Now: [More choices...](#)Tools: Add to Work File: [Create new Work File](#)View: Jump to: [Top](#)[Email th](#)

Title: **CA2310275AA: APPARENT NETWORK INTERFACE FOR AND BETWE
EMBEDDED AND HOST PROCESSORS**

Country: **CA** CanadaKind: **AA** Laid-open Application ¹

Inventor: **BALLANTYNE, TODD ANDREW**; , United States of America
WILT, MICHAEL J.; , United States of America

Assignee: **ACUITY IMAGING, LLC.**, United States of America
[News, Profiles, Stocks and More about this company](#)

Published / Filed: **June 3, 1999 / Nov. 12, 1998**Application Number: **CA1998002310275**IPC Code: **G06F 13/00**;ECLA Code: **None**

Priority Number: **Nov. 26, 1997 US1997000066583**
Feb. 25, 1998 US1998000030411
Nov. 12, 1998 WO1998000024362

Abstract:





An apparent network interface (30) permits an embedded processor (20) to communicate to a host processor (2) using standard network communication mechanisms/protocols such as TCP/IP, NFS, FTP, HTTP, etc. The web server protocol HTTP is particularly useful because it permits the embedded computer to publish a user interface for remote monitoring and remote control using a standard web browser application. The invention provides the host computer(10) with an apparent network interface (30) that appears to be a standard network device, such as an Ethernet interface card. This apparent network interface (30) communicates directly with the embedded processor (20) which appears to be a device on the apparent network. Significant cost savings and performance enhancements are realized by implementing the communication directly over the host computer's peripheral bus (6) rather than using standard network hardware such as Ethernet hardware.

INPADOC
Legal Status:

Gazette date	Code	Description (remarks)	List all possible codes for CA
May 16, 2000	REFW	Corresponds to pct application	

Designated Country: **AL AM AP AZ BA BB BG BR BY CA CN CU CZ EA EE GE GH GM HU ID IL IS KE
KG AT BE CH CY DE DK ES FI FR GB GR IE IT**

Family:

Buy PDF	Patent	Pub. Date	Filed	Title
	WO9927456A1	June 3, 1999	Nov. 12, 1998	APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS
	US20020019924A1	Feb. 14, 2002	Oct. 15, 2001	Flexible processing hardware architecture
	US6308234	Oct. 23, 2001	Feb. 25, 1998	Flexible processing hardware architecture
	US6058434	May 2, 2000	Feb. 19, 1998	Apparent network interface for and between embedded and host processors
	JP2001524713T2	Dec. 4, 2001	Nov. 12, 1998	
	EP1032884A1	Sept. 6, 2000	Nov. 12, 1998	APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS
	CA2310275AA	June 3, 1999	Nov. 12, 1998	APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS
	AU1460199A1	June 15, 1999	Nov. 12, 1998	APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS
	AU0743997B2	Feb. 14, 2002	Nov. 12, 1998	APPARENT NETWORK INTERFACE FOR AND BETWEEN EMBEDDED AND HOST PROCESSORS
9 family members shown above				

Other Abstract Info:

DERABS G1999-404771



Nominate this for the Gallery

© 1997-2002 Delphion, Inc.

Research Subscriptions | Privacy Policy | Terms & Conditions | Site Map | Contact Us